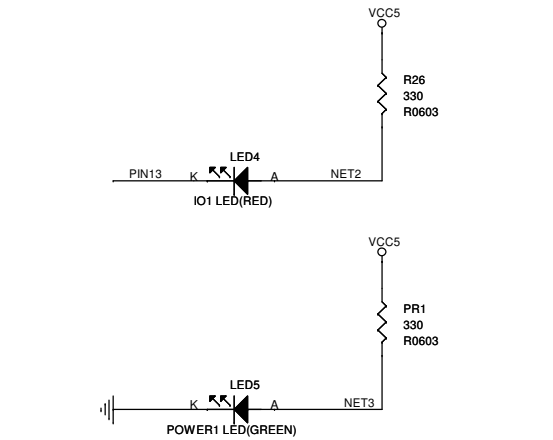
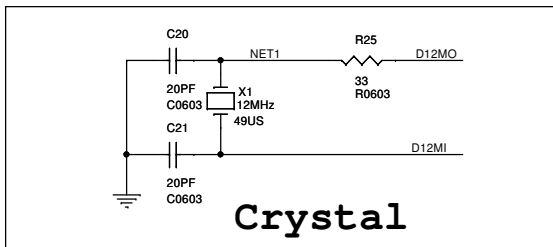
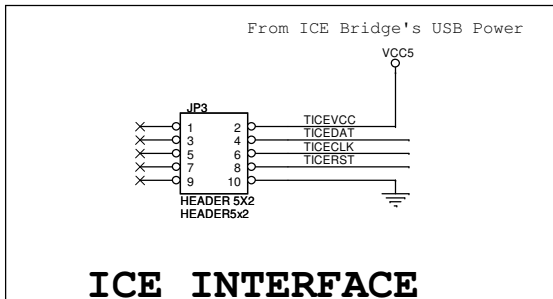
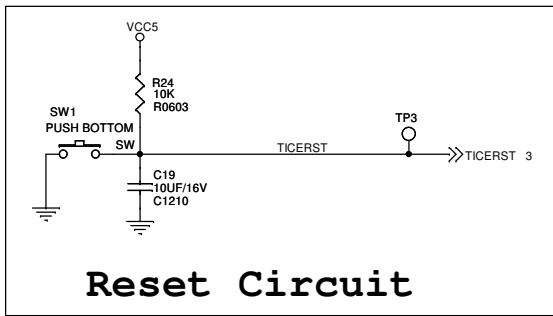


32QFN DM9051 DEMO BOARD V1.1

P/N: D4L-9051-NN-11-2

Davicom Semiconductor Inc.			
Title			
TOP			
Size	Document Number		Rev
A4	DM9051_UNIVERSAL		1.1
Date:	Tuesday, January 26, 2016	Sheet	1 of 4



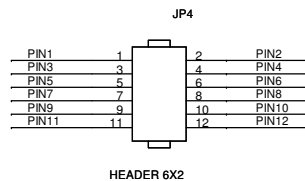
DEBUG UART  
PIN 5 RXD0  
PIN 7 TXD0  
=====

APPLIANCE I/F  
SPI SLAVE  
PIN 8 PIND  
PIN 47 SPISS0  
PIN 1 MOSI\_0  
PIN 2 MISO\_0  
PIN 3 SPICLK0

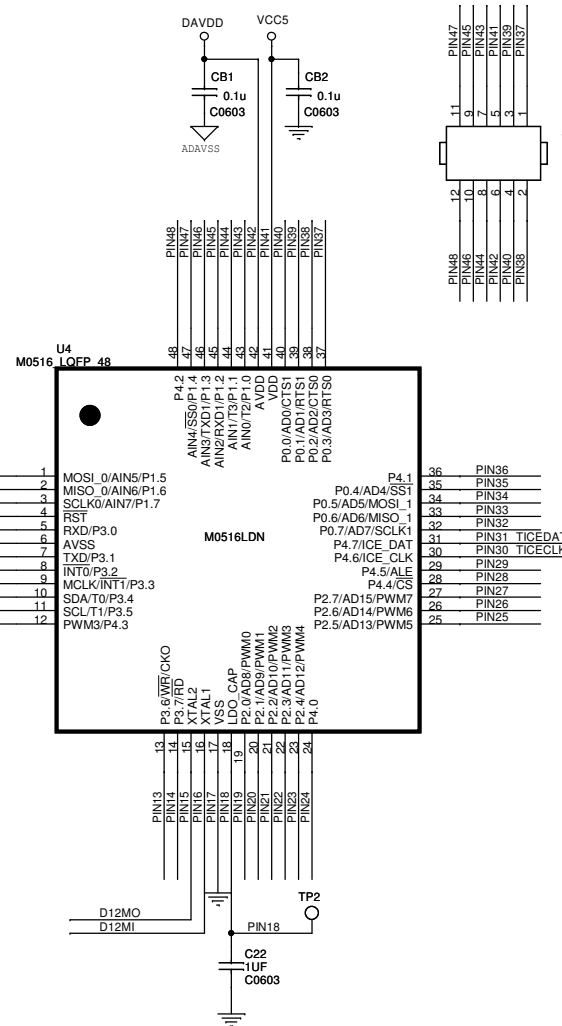
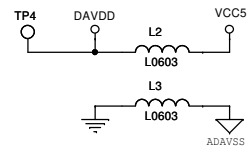
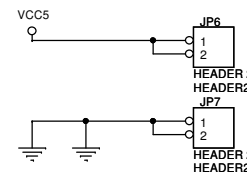
I2C SLAVE  
PIN 10 SDA0  
PIN 11 SCL0

UART  
PIN 45 RXD1  
PIN 46 TXD1

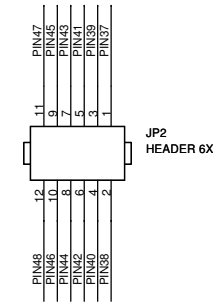
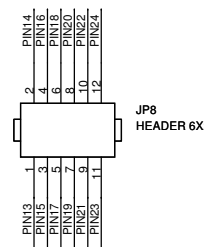
JP4 pin text:  
pin no. = text  
1 = 1  
2 = 2  
3 = 3  
4 = 4  
5 = 5  
6 = 6  
7 = 7  
8 = 8  
9 = 9  
10 = 10  
11 = 11  
12 = 12



TIGERST  
ADAVSS

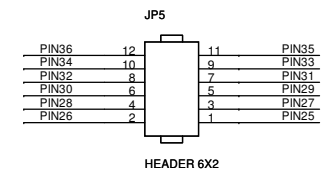


JP8 pin text:  
pin no. = text  
1 = 13  
2 = 14  
3 = 15  
4 = 16  
5 = 17  
6 = 18  
7 = 19  
8 = 20  
9 = 21  
10 = 22  
11 = 23  
12 = 24



JP2 pin text:  
pin no. = text  
1 = 37  
2 = 38  
3 = 39  
4 = 40  
5 = 41  
6 = 42  
7 = 43  
8 = 44  
9 = 45  
10 = 46  
11 = 47  
12 = 48

PIN36 >>> SPI\_INT 3  
PIN35 >>> SPISS1 3  
PIN34 >>> MOSI\_1 3  
PIN33 >>> MISO\_1 3  
PIN32 >>> SPICLK1 3



JP5 pin text:  
pin no. = text  
1 = 25  
2 = 26  
3 = 27  
4 = 28  
5 = 29  
6 = 30  
7 = 31  
8 = 32  
9 = 33  
10 = 34  
11 = 35  
12 = 36



VER	DATE	ENGINEER	NOTE
1.0	10/12/2015	Willie Niou	INITAL CIRCUIT CREATION
1.1	01/26/2016	Willie Niou	Add JP9 3.3V and GND connector