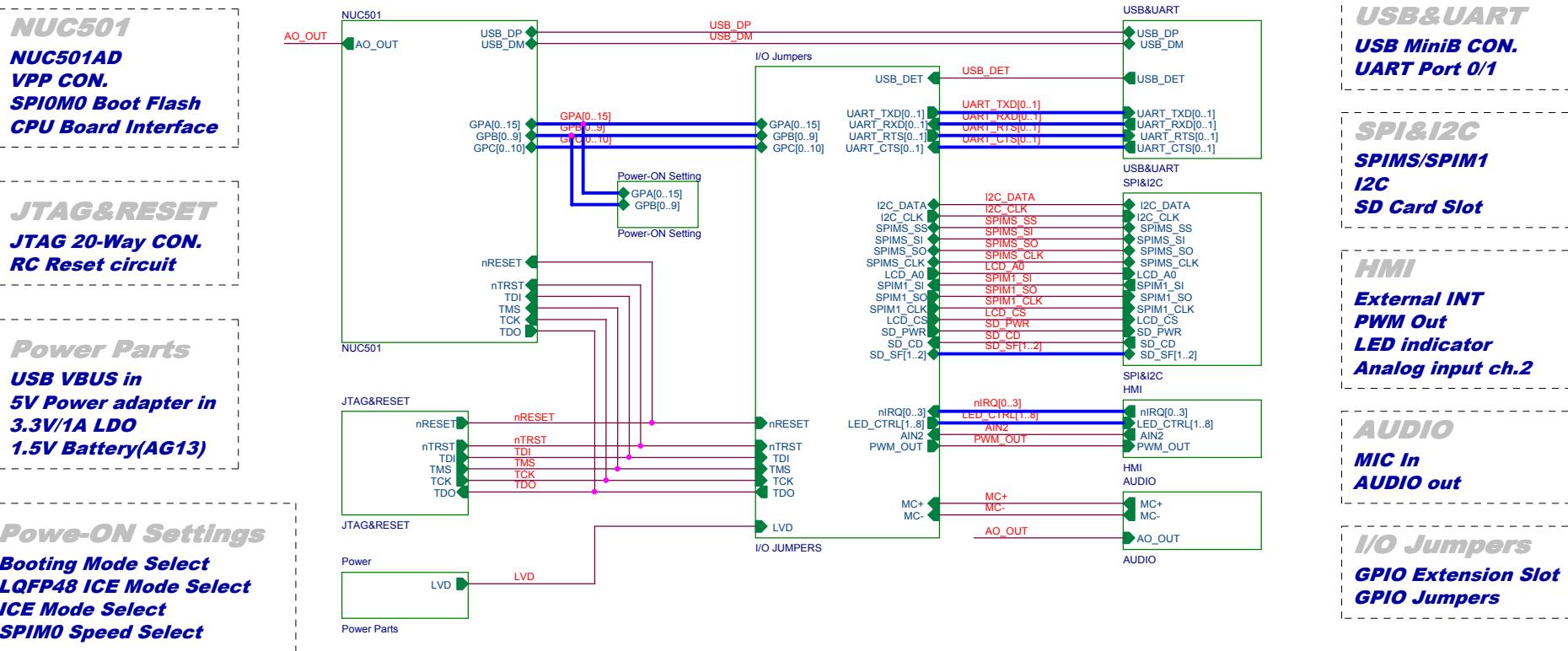


# /// NUC501 DEV Board Ver1.2 ///



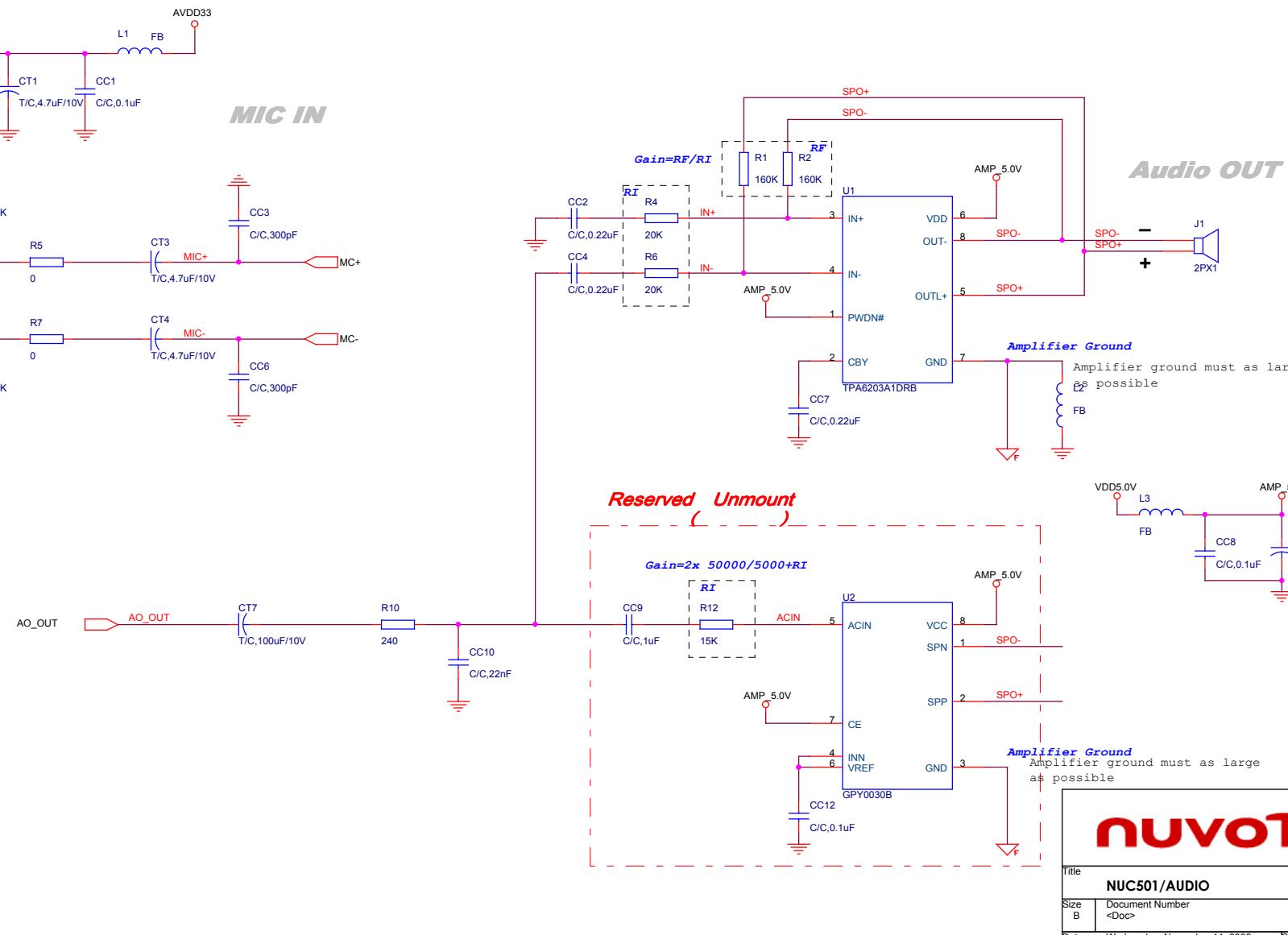
**nuvoton**

Title: NUC501/TOP Block

Size: B Document Number: <Doc>

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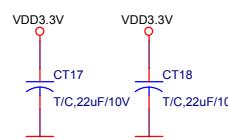
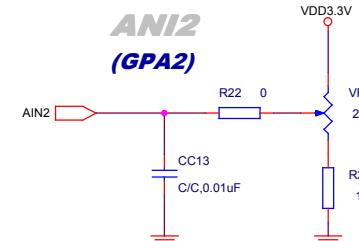
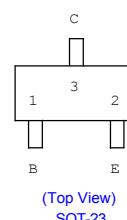
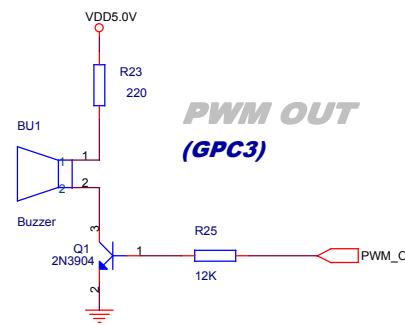
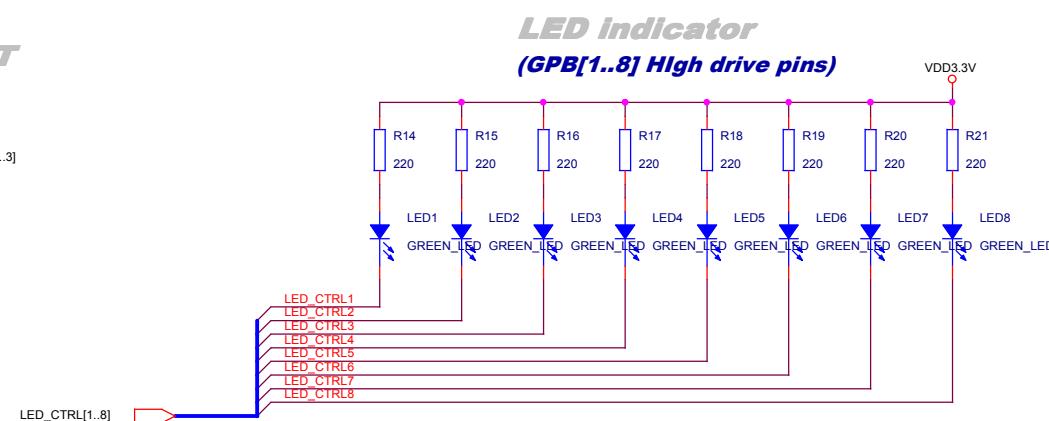
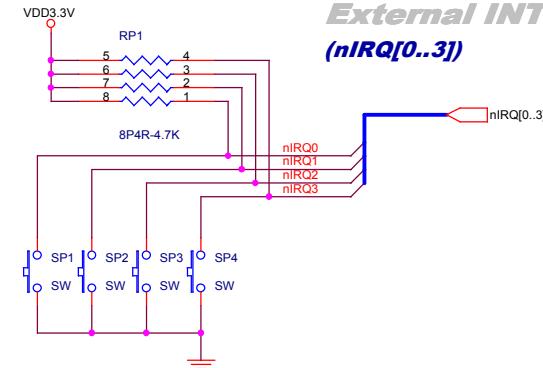
**nuvoton**

Title: **NUC501/AUDIO**

Size: **B** Document Number: <Doc>

Rev: **A**

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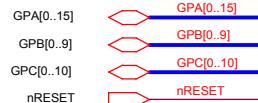
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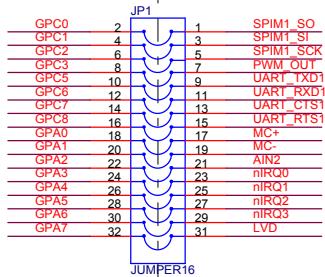
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# /// I/O Jumpers ///



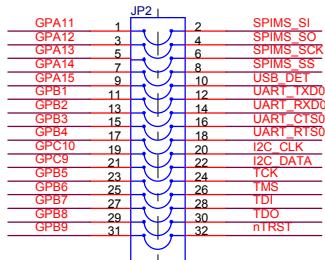
## GPIO Port A/B/C      Function Select



**SPIM1**  
**PWM Out**  
**UART Port 1**

**MIC**  
**AIN2**  
**IRQ**

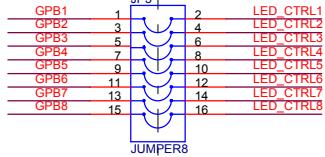
**Low Voltage Detect**



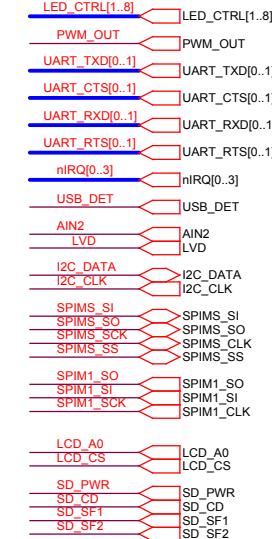
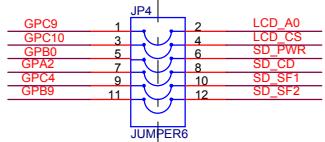
**SPIMS**  
**USB Dectect**  
**UART Port 2**

**I2C**  
**JTAG**

**LED Control**



**LCD Control**  
**SD Control**



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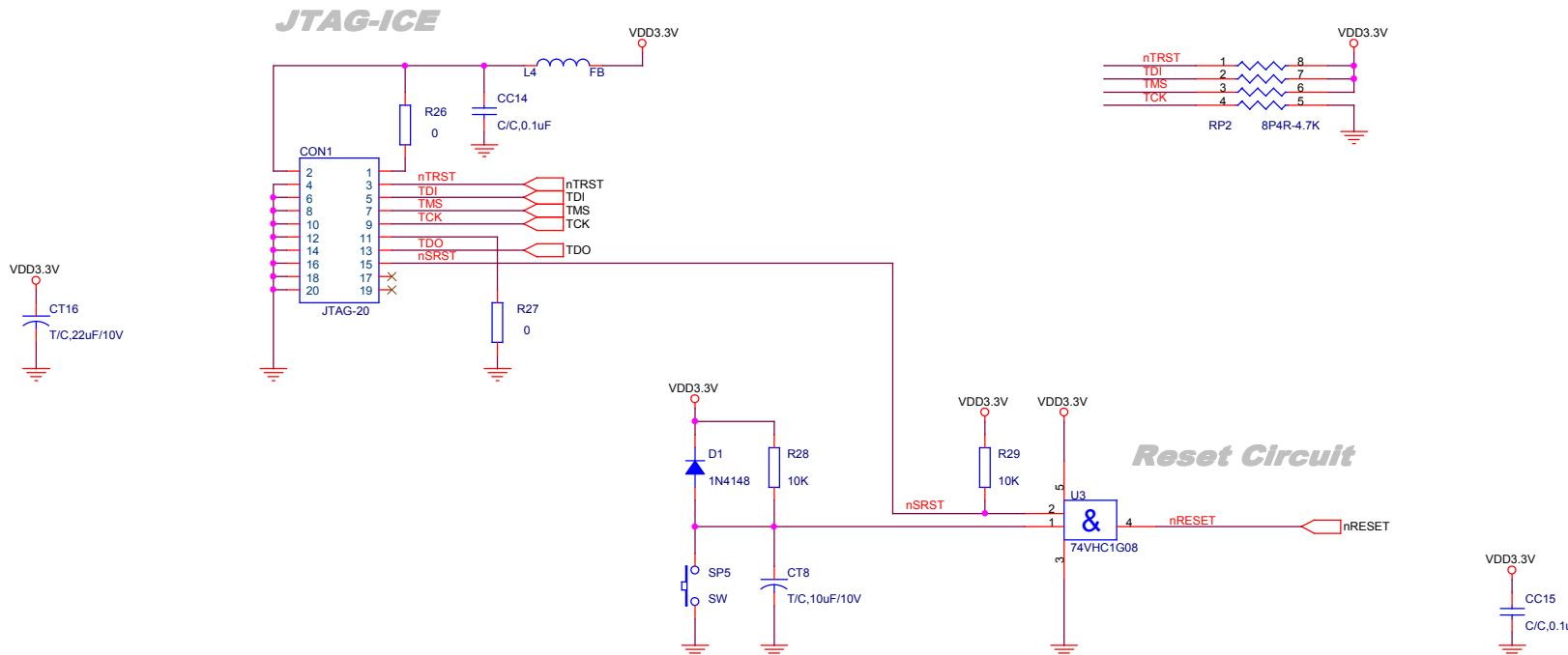
Title: NUC501/I/O Jumpers

Size: B      Document Number: <Doc>

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# /// JTAG&RESET ///



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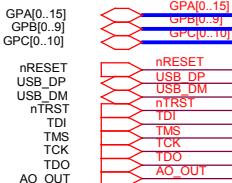
Title: NUC501/JTAG&RESET

Size: B Document Number: <Doc>

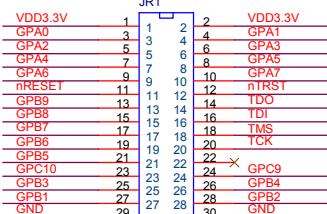
Rev: A

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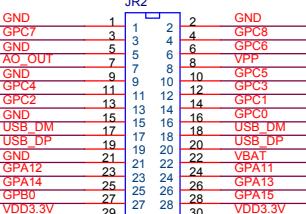
# /// NUC501 ///



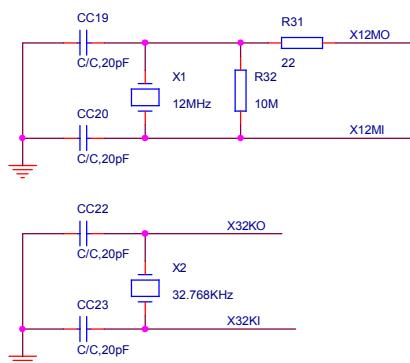
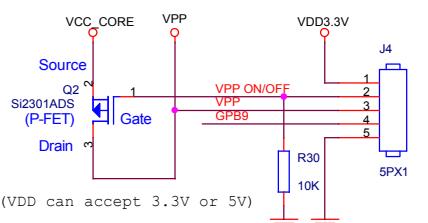
nRESET  
USB\_DP  
USB\_DM  
nTRST  
TDI  
TMS  
TCK  
TDO  
AO\_OUT



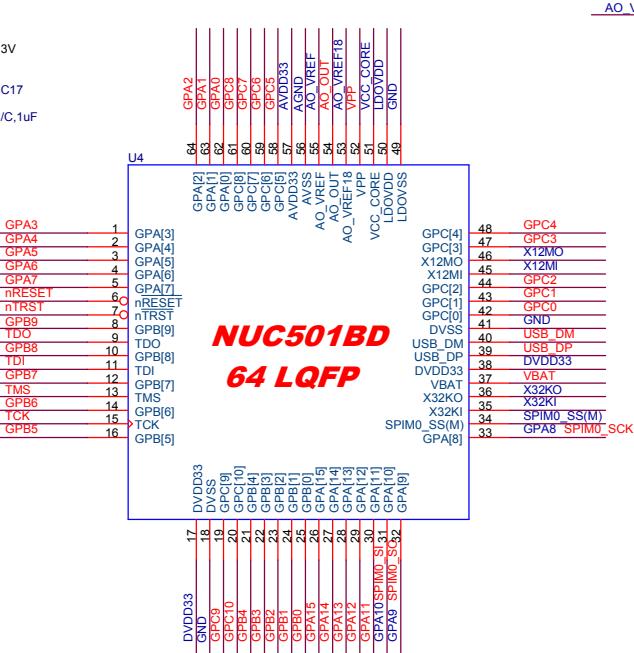
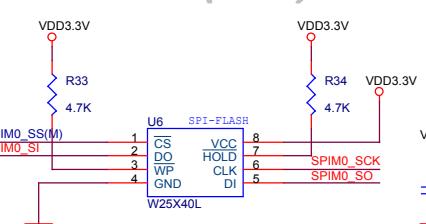
CONN RECT 15x2



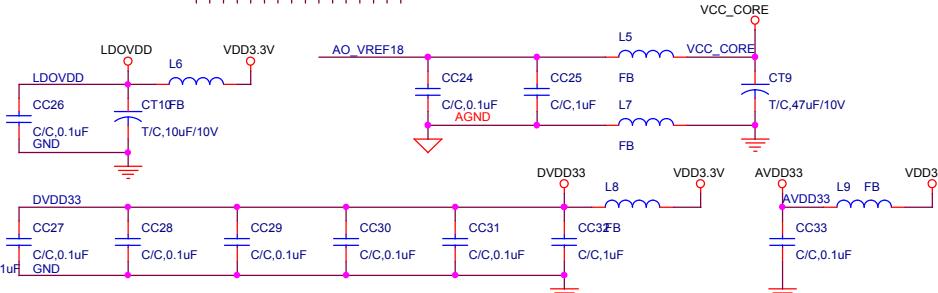
CONN RECT 15x2



## SPIMO(Boot)



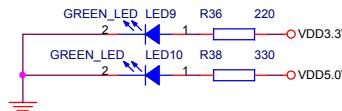
**NUC501BD**  
**64 LQFP**



**nuvoTon**

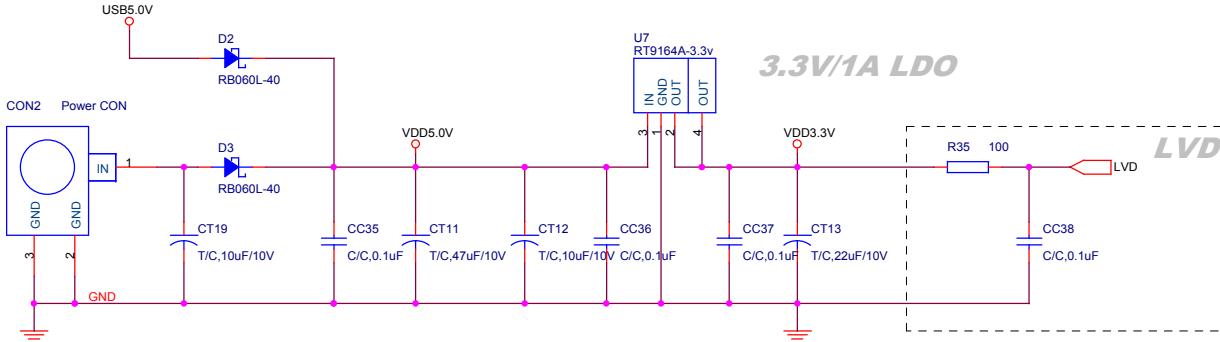
Title	NUC501	Rev	A
Size	B	Document Number	<Doc>
Date:	Wednesday, November 11, 2009	Sheet	6 of 10

*Power Good indicator*



*5V/500mA USB\_BUS Power*

*5V Power Adapter*

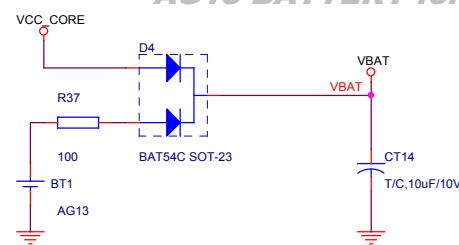
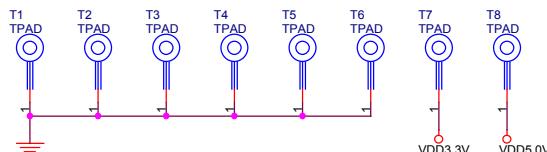


*3.3V/1A LDO*

*LVD*

*AG13 BATTERY for RTC*

*Testing Pad*



Switch power source from BATTERY to VCC\_CORE(1.8V)  
when system power ON.

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Title

NUC501/Power Parts

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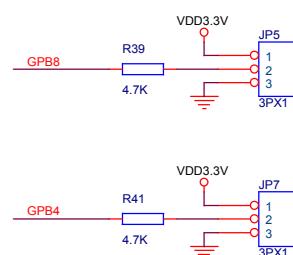
Date: Wednesday, November 11, 2009

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# /// Power-ON Settings ///

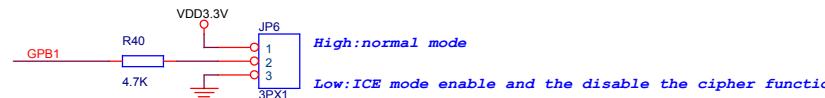
GPA[0..15]      GPA[0..15]  
 GPB[0..9]      GPB[0..9]

## SPI0 Speed Configuration

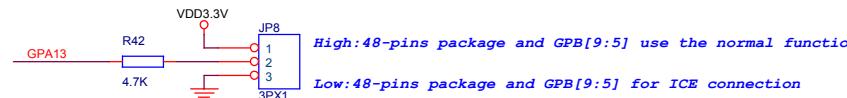


GPB8/GPB4	SPI Speed
(0, 0)	72MHz
(0, 1)	36MHz
(1, 0)	18MHz
(1, 1)	50KHz

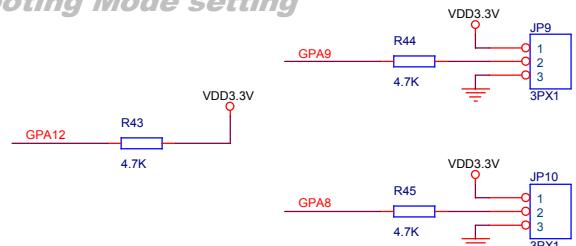
## ICE Mode configuration setting



## LQFP48 ICE mode configuration setting



## Booting Mode setting



GPA12=1 GPA9/GPA8	Function
(0/0)	Boot from SRAM
(0/1)	Boot from USB
(1/0)	OTP program mode
(1/1)	Boot from SPI

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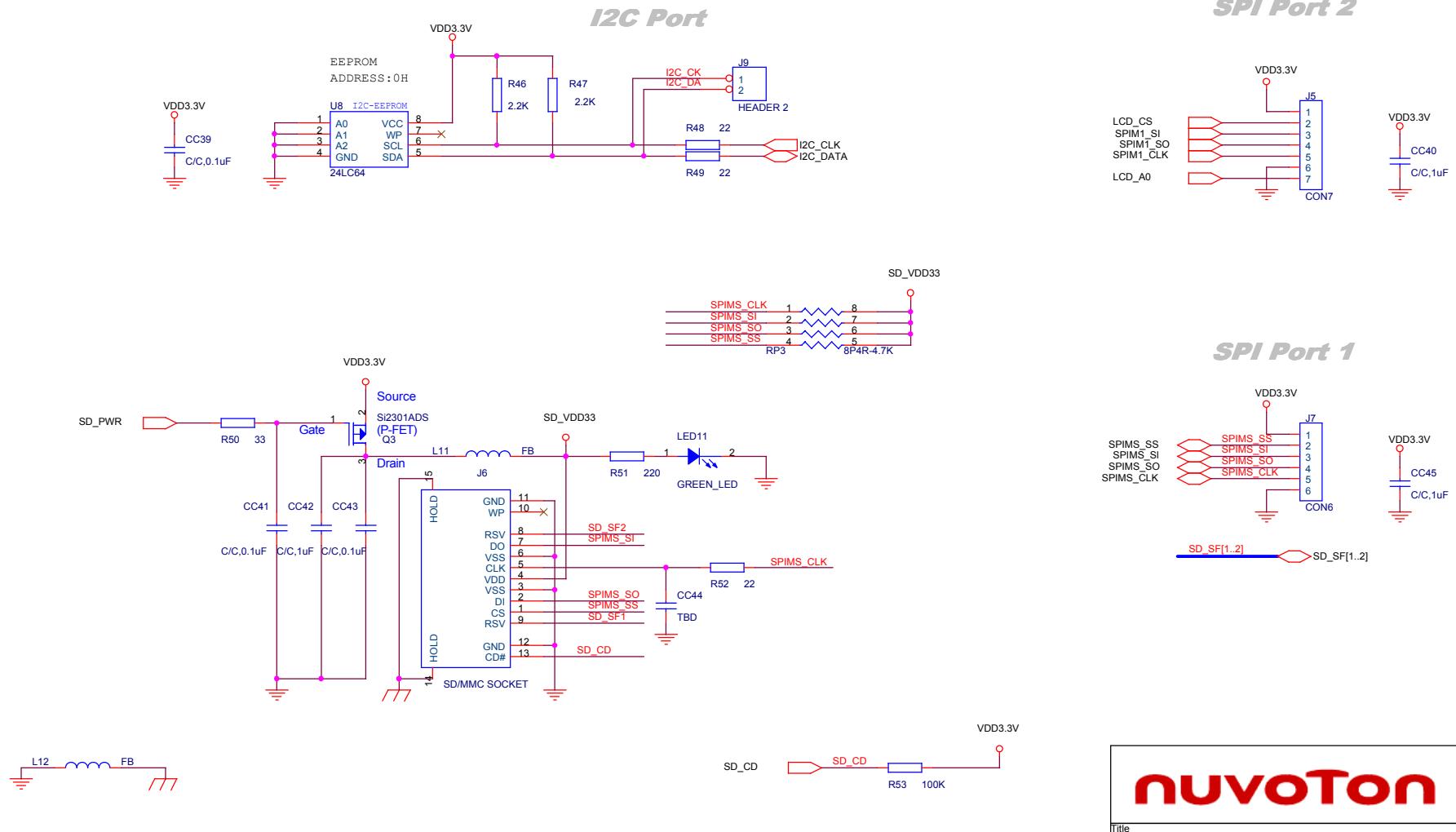
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Size: B Document Number: <Doc>

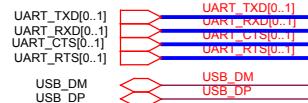
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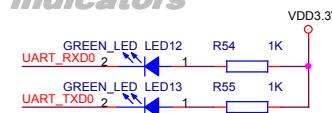
# /// SPI&I2C ///



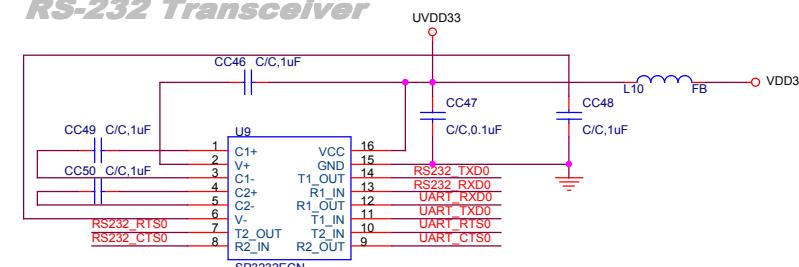
# /// USB&UART ///



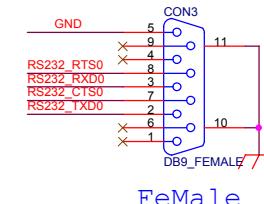
## UART0 TX/RX Indicators



## RS-232 Transceiver



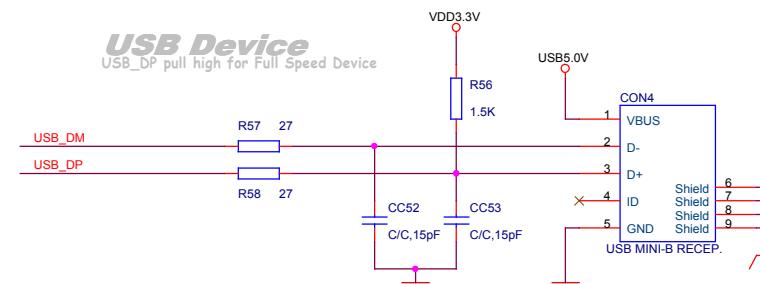
## UART Port 0



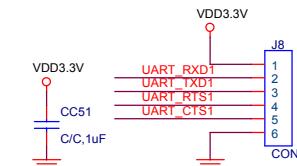
FeMale

## USB Device

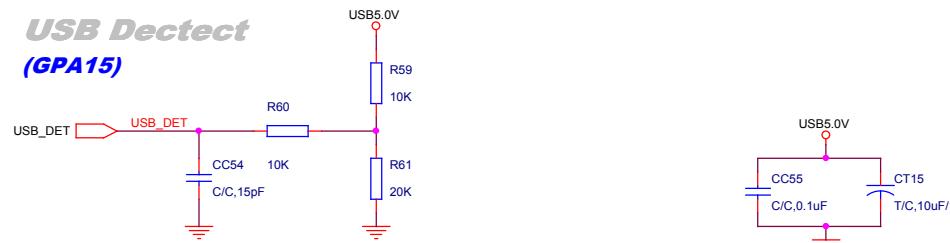
USB\_DP pull high for Full Speed Device



## UART Port 1



## USB Detect (GPA15)



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Title: NUC501/USB&UART

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